Notice of Allowability	Application No.	Applicant(s)	
	10/051,836	HSIEH, KUO-CHIANG	
	Examiner	Art Unit	m
	Tuan T. Lam	2816	18.
The MAILING DATE of this communication appeal claims being allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIP of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communicatio IGHTS. This application is subject to and MPEP 1308.	oplication. If not included n will be mailed in due cou	rse. <b>THIS</b>
1. A This communication is responsive to the communication file	<u>ed 1/16/2002</u> .		
2. ⊠ The allowed claim(s) is/are <u>1-26</u> .			
3. $igotimes$ The drawings filed on <u>26 January 2002</u> are accepted by the	e Examiner.		
4.			
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ⊠ Examiner's Amend	ate	,
	PRIM	ARY EXAMINER	

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1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

## Claim 22:

line 2, "differential clock circuitry" has been changed to --first and second differential clock circuitries--.

The changes have been made to provide proper antecedent basis for the first and second differential clock circuitries as recited in claim 21.

## Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance: the prior art of record fails to teach or fairly suggest second sampling circuitry invertedly connected to the data input and connected to the data output for the second duration to provide the second data bit inverted to the data output and holding circuitry connected to the data output for the second duration whereby the holding circuitry holds the first data bit and the second sampling circuitry connects the second data bit inverted to the data output to enhance the held first data bit when the first and second data bits have different states as called for in claims 1, 11 and 21. Therefore, claims 1-26 are presently allowed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam Primary Examiner

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11/22/2004